

## Inventor: Toshio ITO

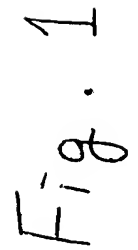


Fig. 1

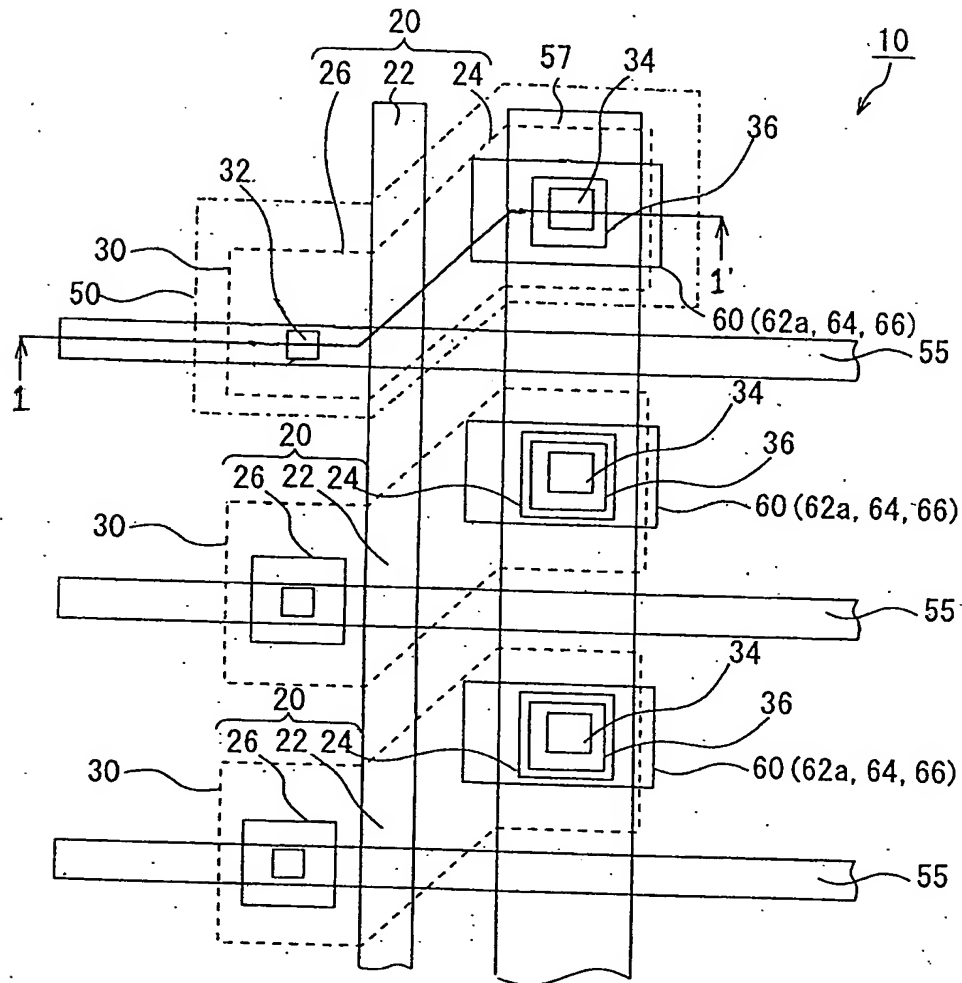


Fig. 2

Application No. [New] – Attorney Doc. No. OKI.639  
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B)

This diagram shows a cross-sectional view of a semiconductor device assembly. A substrate 12 is at the base, with a layer 19 on top of it. A central block 13 is mounted on the substrate. To the left of block 13 is a structure 20, which includes a central core 22 flanked by regions 26 and 24. A layer 14 is positioned below the central core 22. Above the central block 13, there is a stack of layers labeled 66, 64, 63, 62b, and 62a. A bracket labeled 60 groups these layers. A bracket labeled 62 groups layers 62b and 62a. A layer 17 is located between the central block 13 and the stack of layers. A layer 32 is on the left side, and a layer 34 is on the right side. A layer 55 is on the far left. A dashed line indicates a horizontal interface between the central block 13 and the stack of layers. Labels 'a' and 'b' are placed near the interface. A bracket labeled 'J' is on the left side, pointing to the stack of layers. A bracket labeled '14' is at the bottom, pointing to the layer 14.

Fig. 5 (A)

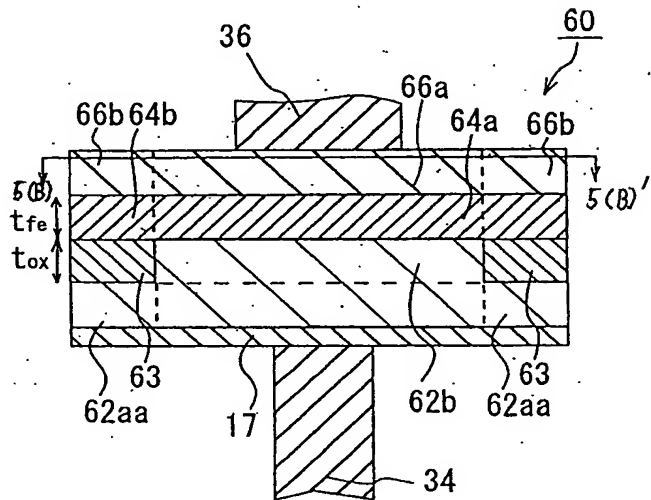


Fig. 5 (B)

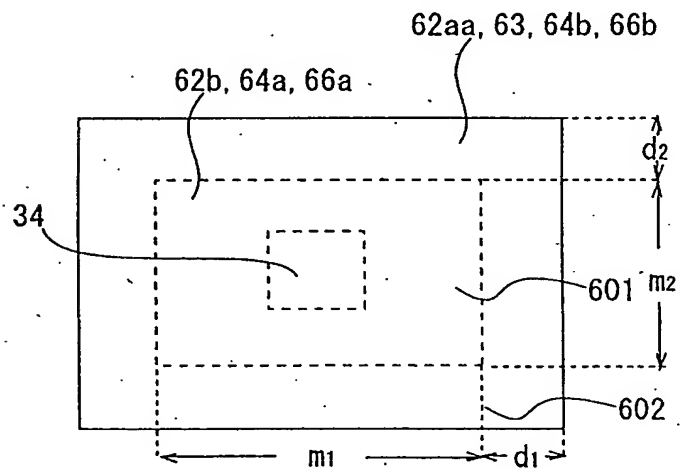
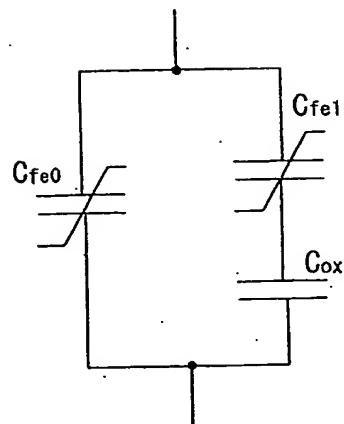


Fig. 5 (c)



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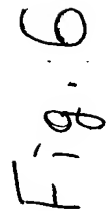


Fig. 7(A)

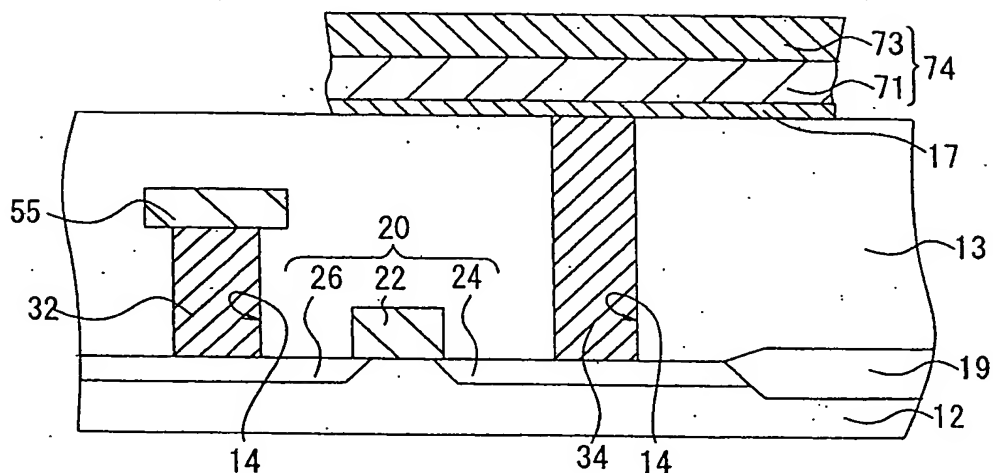


Fig. 7(B)

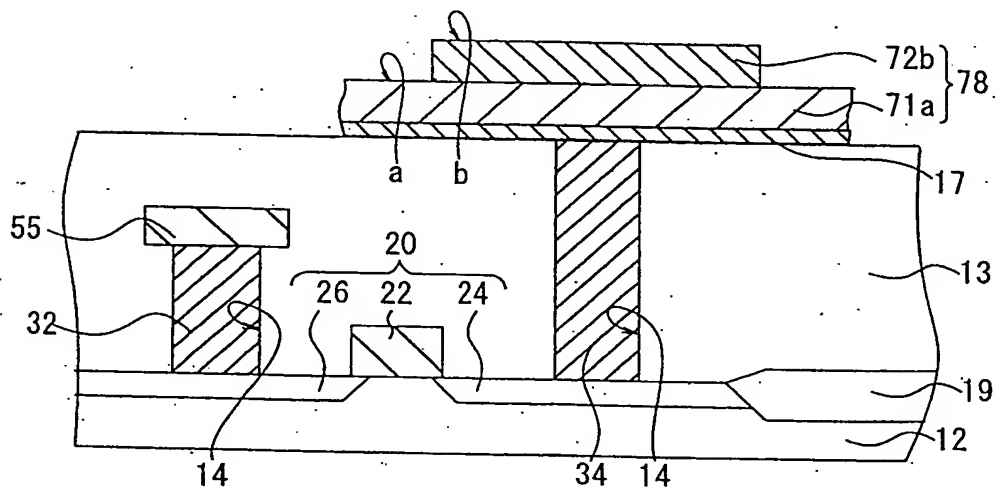
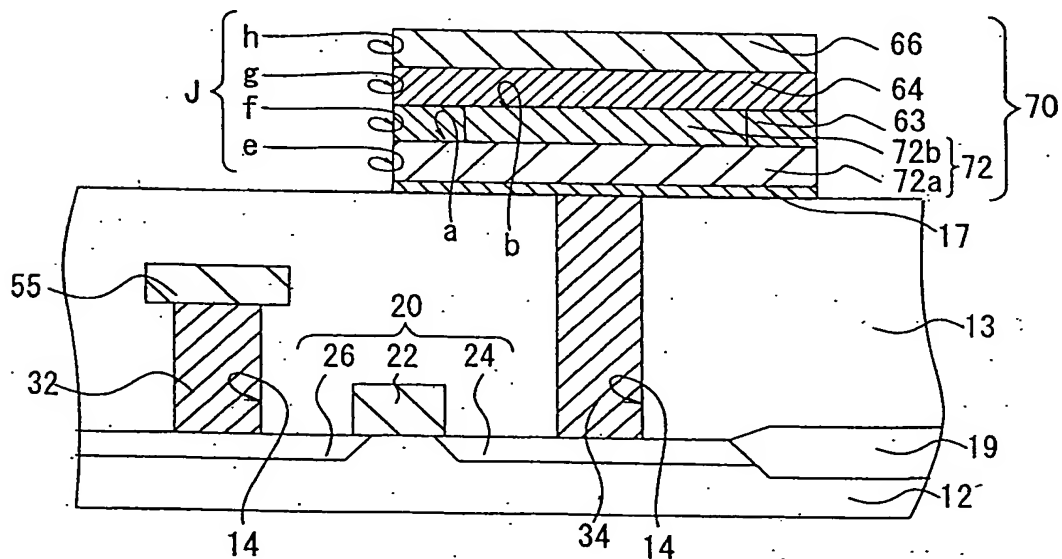


Fig. 7(C)



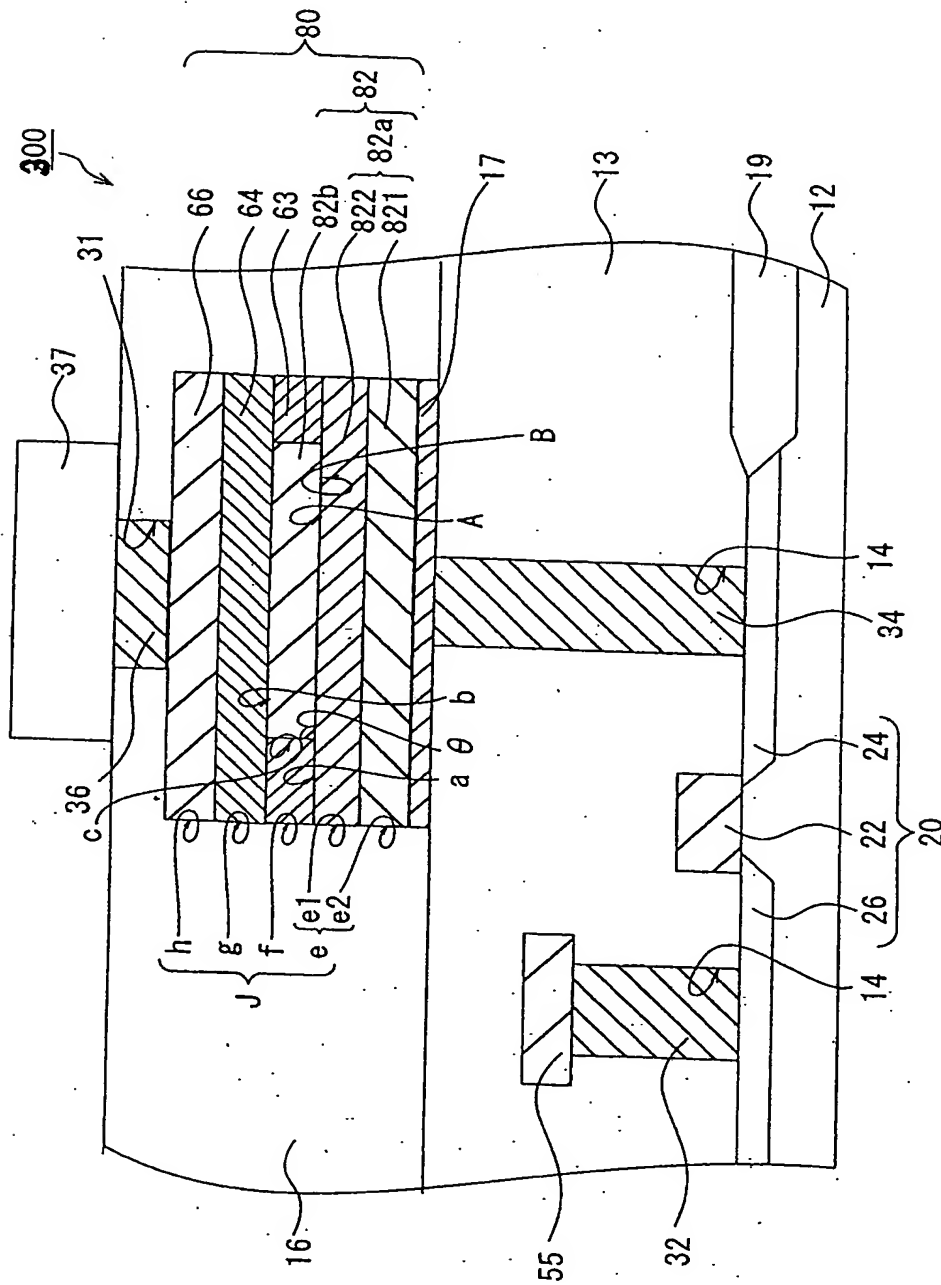


Fig. 8



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[illegible]

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Fig. 10

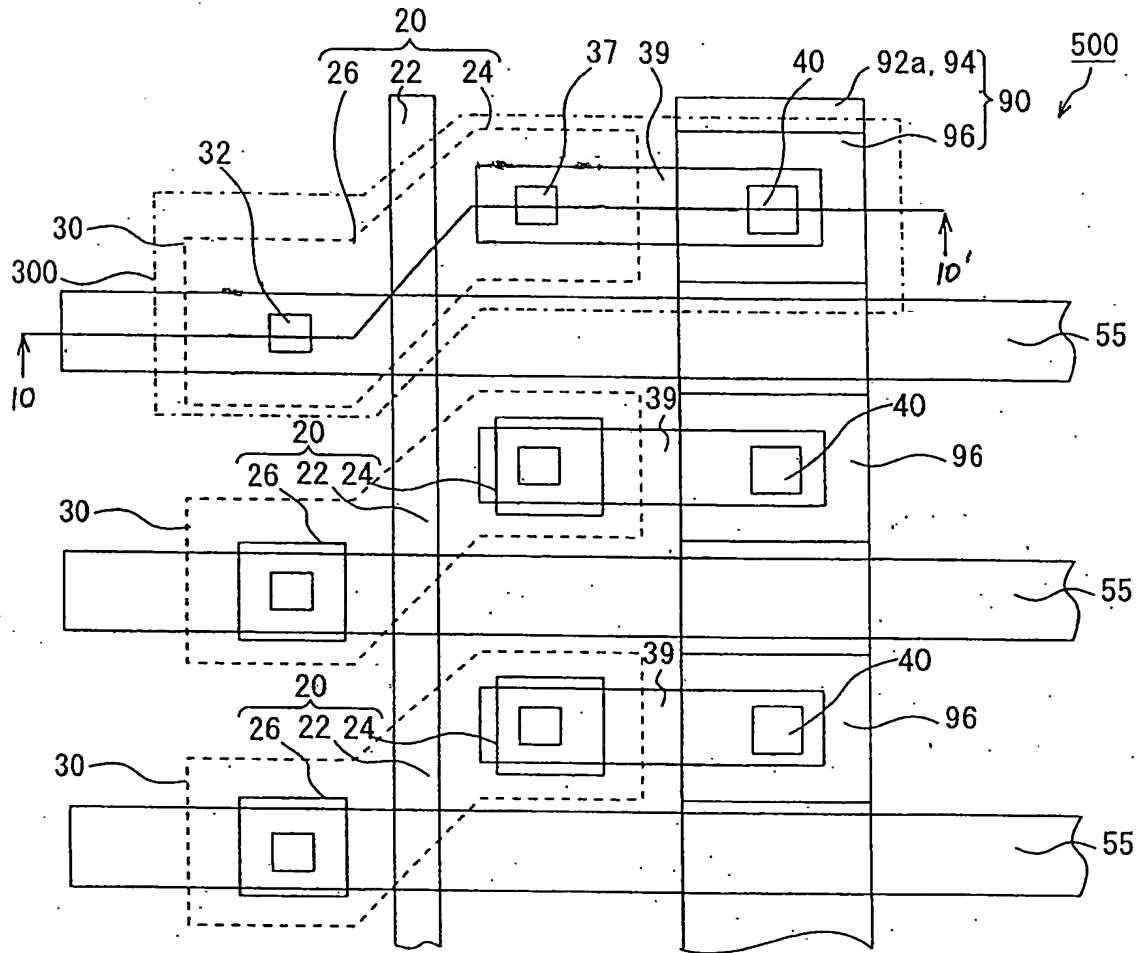


Fig. 11

Fig.12(A)

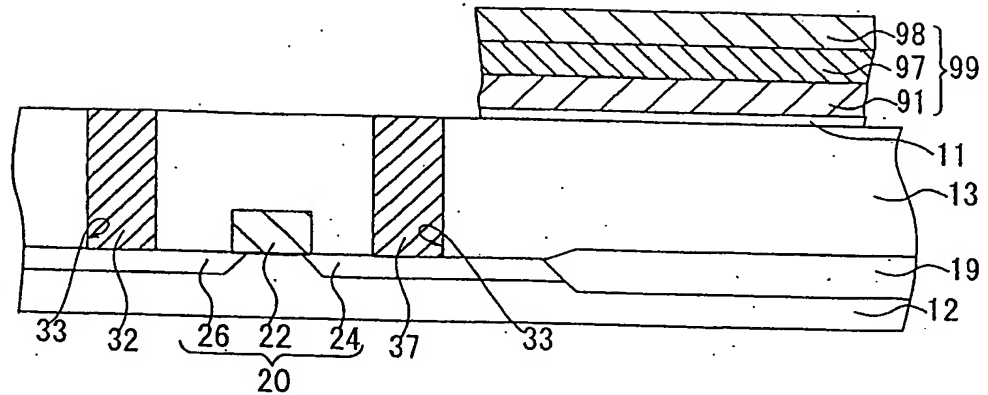


Fig.12 (B)

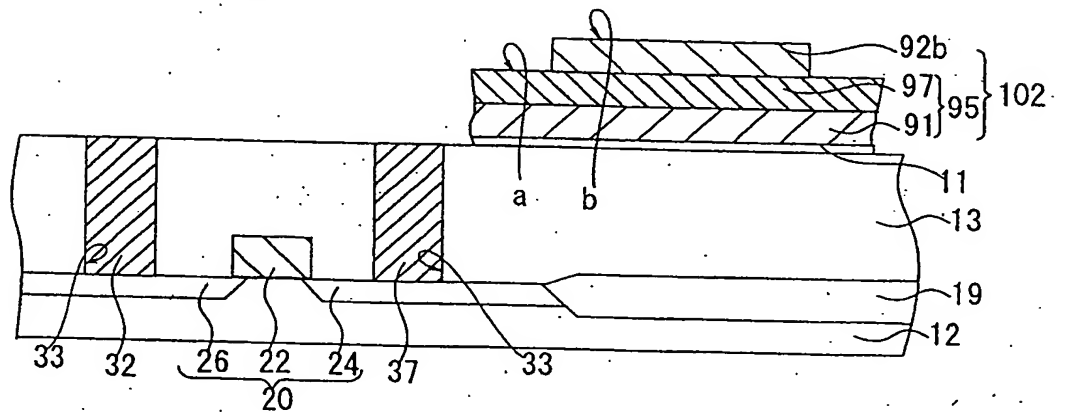


Fig.12 (c)

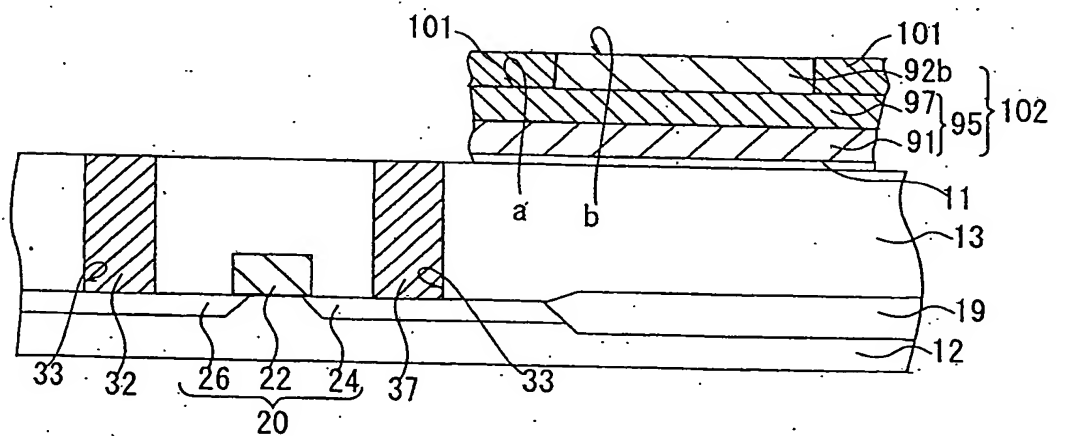


Fig. 13 (A)

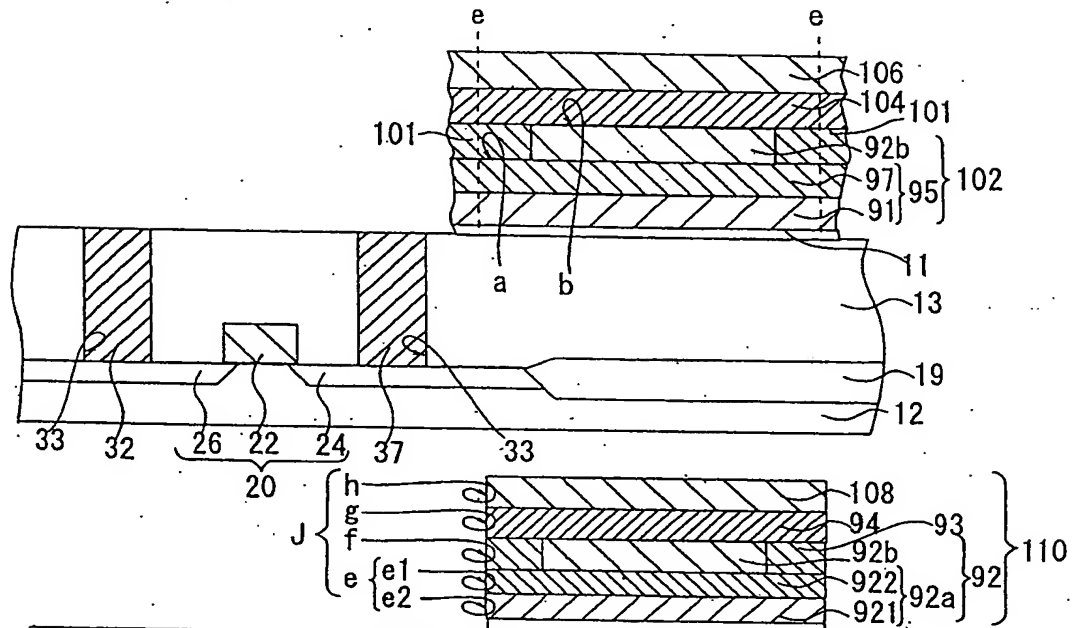


Fig. 13 (B)

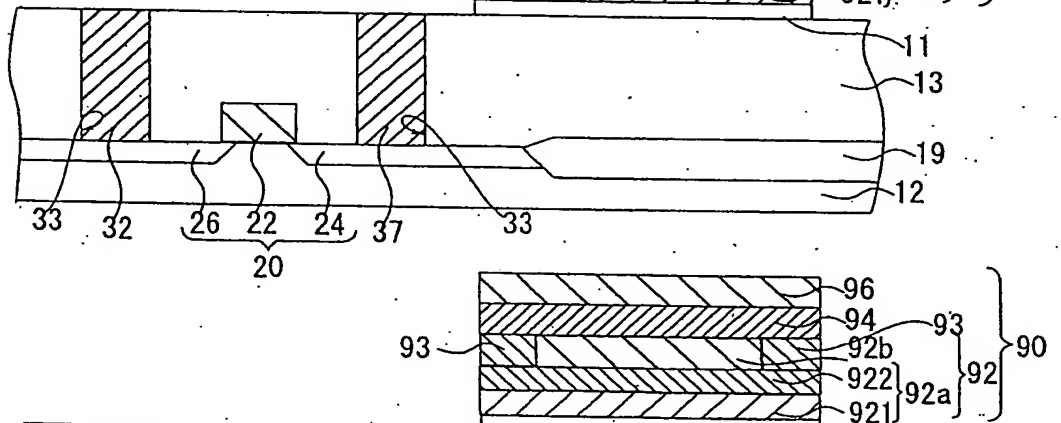


Fig. 13 (C)

